

2016, November 30th - December 2nd

IT Center, RWTH Aachen University

2016	Wed, Nov 30	Thu, Dec 1	Fri, Dec 2
rooms	Seminar room 3/4	Seminar room 3/4	Seminar room 3/4
10:00-11:00	PART I Welcome + Introduction (C Terboven, RWTH, 15 min) Claix System configuration (F Unger, NEC, 15 min) Intel OmniPath Fabric and Intel MPI (C Dahnken, Intel, 30 min)	9:00 - 10:30 Intel Tuning Workflow (M. Klemm, Intel) Hands-on	9:00 - 10:30 Transactional Memory (M Klemm, Intel) Hands-on
11:00 - 11:30	coffee break	10:30 - 11:00	10:30 - 11:00
11:30 - 12:30	Intel Processor Architectures: Broadwell and Xeon Phi (KNL) (C Dahnken, Intel, 60 min)	11:00 - 12:30	11:00 - 12:30
12:30-14:00	lunch	12:30-14:00	12:30-14:00
14:00 - 15:30	Intel Software for HPC: Compiler, Amplifier XE, Advisor XE, SIMD Vectorization with OpenMP Performance Analysis w/ VTune Amplifier XE (M Klemm, Intel, 90 min)	14:00 - 15:30	14:00 - 15:30
15:30 - 16:00	coffee break	15:30 - 16:00	15:30 - 16:00
16:00 - 17:30	PART II Lab System Overview (P Kapinos, RWTH, 15 min.) Hands-on	16:00 - 17:30 Hands-on Case study Wrap-up	16:00 - 17:30 Visionary Closing Remarks Hands-on (Case study) wrap-up / feed-back
		19:00	
		Social Event Restaurant Palladion	