

aiXcelerate 2017

December 5-7, 2017

IT Center, RWTH Aachen University
Kopernikusstr 6, Seminar room 3/4

	Tue, Dec 5, 2017		Wed, Dec 6, 2017		Thu, Dec 7, 2017
		9:00-10:00	Tuning Workshop	9:00-10:30	VTune for MPI (Michael Klemm, Christopher Dahnken, Intel)
10:00 - 12:30	MPI Tuning and Intel Omnipath Fabric Intel Tuning Tools ITAC, Intel APS (Michael Klemm, Christopher Dahnken, Intel)	10:00 - 13:00	Intel Xeon Phi Processor Architecture (KNL) (Michael Klemm, Christopher Dahnken, Intel)	10:30-12:30	Tuning Workshop
12:30 - 13:00	non-Intel Tuning Tools (Dirk Schmidl, RWTH) Open MPI (Hristo Iliev, RWTH)				
13:00-14:00	lunch	13:00-14:00	lunch	12:30-14:00	lunch
14:00-18:00	Introduction Workshop Environment (Paul Kapinos, RWTH) Tuning Workshop	14:00-18:00	Tuning Workshop	14:00-17:00	Tuning Workshop
		18:30	Social Dinner for Workshop Participants		