

Webaddresses

Home page of the Center:

<http://www.rz.rwth-aachen.de>

High Performance Computing:

<http://www.rz.rwth-aachen.de/hpc>

Phone Numbers

Reception: +49 (0) 241/80-24900

HPC Support: +49 (0) 241/80-24915

Address

Center for Computing and Communication
RWTH Aachen University
Seffenter Weg 23
D-52074 Aachen
Germany

Secretary: +49 (0) 241/80-29101

Fax: +49 (0) 241/80-22241

E-mail: sekretariat@rz.rwth-aachen.de

Head of the Center

Prof. Christian Bischof, Ph.D.



Directions

By car: From Cologne (A4) or Düsseldorf (A44) to the highway interchange "Aachener Kreuz", then A4 direction Netherlands, exit **Aachen-Laurensberg**. Turn right at the traffic lights, exit "Klinikum", then exit again "RWTH-Hörn", turn left, "Seffenter Weg", until you reach the junction with "Kopernikusstraße" (6th street).

By plane: There are train connections from the airports of **Düsseldorf** (90 km), **Cologne** (85 km), **Frankfurt** (250 km) and **Brussels** (143 km).

By train: The train station **Aachen West** is a 10-minutes walk to the Center.

Bus connections: Bus route **3a** connects the main station and the stop "**Mies-van-der-Rohe-Straße**" every 15 minutes. Bus route **33** connects the city and the stop "**Mies-van-der-Rohe-Straße**". To go back to city or main station please take bus route **3b** (every 15 minutes).

See also: <http://www.aachen-tourist.de>



Seminar
03 ♦ 2009

Parallel Programming in
Computational Engineering & Science
<http://www.rz.rwth-aachen.de/ppces>

Time

Start: Monday, March 23, 14:00

End: Friday, March 27, 13:00

Location

RWTH Aachen University
Center for
Computing and Communication
Seffenter Weg 23, 52074 Aachen

Sponsored by



Microsoft



Recommendation

On Monday, March 23, 11:00

Dr. Horst D. Simon

will talk about

**Future Directions in High Performance
Computing 2009 – 2018**

at the SuperC, Templergraben 57, 52062 Aachen

HPC Seminar

This event continues the tradition of previous annual SunHPC events taking place in Aachen since 2001. These have been organized by the RWTH Aachen University in collaboration with Sun Microsystems.

We keep evolving the format over the years. With the shift to Intel Xeon based hardware in our Center, we also shift the focus of the tutorials towards Linux and Windows.

After an introduction into the programming development environments and parallel computer architectures, we will cover parallel programming using **OpenMP** and **MPI** in **Fortran** and **C/C++** on **Linux**, **Windows** and **Solaris** platforms. Lectures will be complemented by practical exercises.

Starting on Monday afternoon, we give basic introductions into the programming development environments on the platforms available for the lab exercises during the following days. Those who feel comfortable with their favourite platform are welcome to skip these basic introductions on Monday afternoon or to take the opportunity to get hands on the other ones.

At our Center we try to serve Linux, Windows and Solaris equally well.

Participants

Attendees should be comfortable with C/C++ or Fortran programming and interested in learning more about the technical details of application tuning and parallelization. The presentations will be given in English language.

Speakers

We are happy to welcome **Ruud van der Pas** from Sun Microsystems as the main speaker of the seminar.

We also invited **Andrey Semin** from Intel who will talk about the upcoming Intel Nehalem processor architecture.

Costs

There is no seminar fee. Breaks and the social dinner are kindly sponsored by the companies Sun, Intel and Microsoft.

All other costs are at the expenses of the participants.

Contact

Dieter an Mey

Phone: +49 (0) 241/80-24377

E-mail: anmey@rz.rwth-aachen.de

Registration and further information

Allocation is on a first come, first served basis, as we are limited in capacity. Please register through our website by **March 16, 2009**.

<http://www.rz.rwth-aachen.de/ppces>

Future HPC equipment of our Center

In November 2008, the RWTH Aachen University has again chosen an HPC system from Sun Microsystems. Sun plans to install a 200-Teraflop/s cluster in the Aachen University in two installation phases scheduled for completion at the end of 2010.

The compute nodes will be based on the next-generation Intel Xeon processor code-named "Nehalem" and the Sun Constellation System including a QDR-Infiniband-fabric and a Lustre File System. Meanwhile we are impatiently waiting for the completion of the extension building of the Center to host the new machinery.



A first Nehalem-based Cluster which is expected to be installed in April '09 will be hosted in the existing building.